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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Inventive Application of:

TI-31354

Technology Center 2100

Serge Lasserre

Art Unit: 2188

Serial No: 09/932,381

Examiner: Reginald G. Bragdon

Filed: August 17, 2001

Conf. No.: 4434

For: Local Memory With Indicator Bits to Support Concurrent DMA and CPU Access

INFORMATION DISCLOSURE STATEMENT PURSUANT TO §1.97(c)


Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

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Robin E. Barnum

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the noted references are enclosed herewith.

Applicant is submitting herewith a copy of a communication from a foreign patent office in a counterpart foreign application and copies of the documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This Information Disclosure statement is being submitted before the mailing date of either a Final Action or a Notice of Allowance and the fee set forth in 37 CFR 1.17(p) should be charged to Texas Instruments Incorporated, Deposit Account 20-0668.

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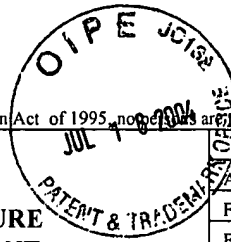
Respectfully submitted,



Robert D. Marshall, Jr.
Attorney for Applicant(s)
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Texas Instruments Incorporated
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PTO/SB/08A (08-00)

Approved for use through 10/31/2002. OMB 0651-0031
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Substitute for Form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete If Known

Sheet	1	of	1
Application Number			
Filing Date	August 17, 2001		
First Named Inventor	Serge Lasserre		
Group Art Unit	2188		
Examiner Name	Reginald G. Bragdon		
Attorney Docket No.	TI-31354		

U.S. PATENT DOCUMENTS

Exam. Initials*	Cite No. ¹	U.S. Patent Document	Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)		
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FOREIGN PATENT DOCUMENTS

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		Office ³	Number ⁴	Kind Code ² (if known)		
	BA	EP	0 529 217 A		Motorola Inc.	3/3/93
	BB					
	BC					
	BD					
	BE					
	BF					
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	IBM Corp.; <i>Use of Dirty, Buffered, and Invalidate Bits for Cache Operations</i> , IBM Technical Disclosure Bulletin; New York, US, vol. 35, no. 1A, 1 June 1992, pg. 124.	
	CB	IBM Corp.; <i>Low End Parallel Bus Memory Refresh Activated Bus Timeout Control</i> , IBM Technical Disclosure Bulletin; New York, US, vol. 31, no. 3, 1 August 1988, pgs. 90-92.	
	CC		
	CD		
	CE		
	CF		
	CG		
	CH		

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

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